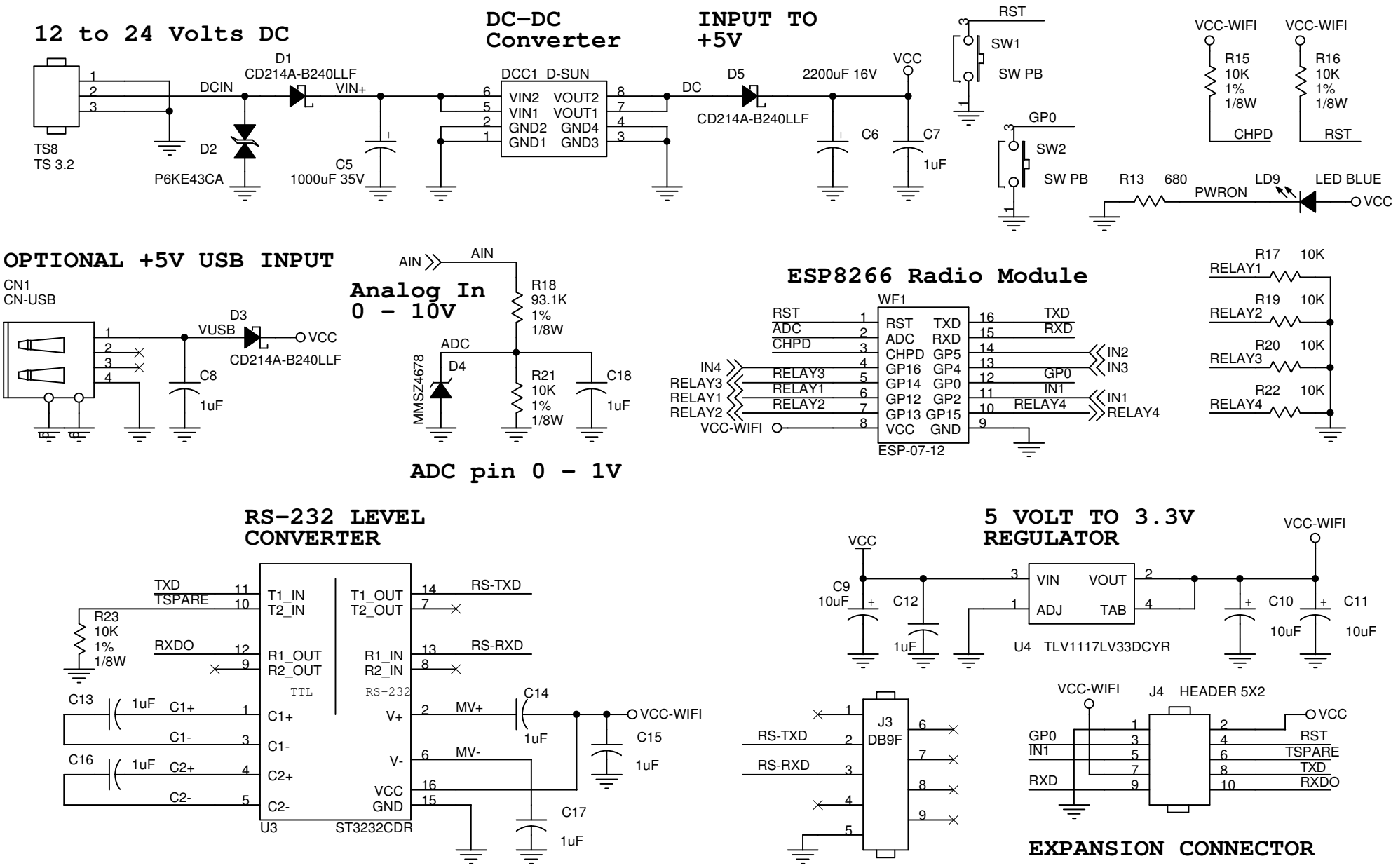
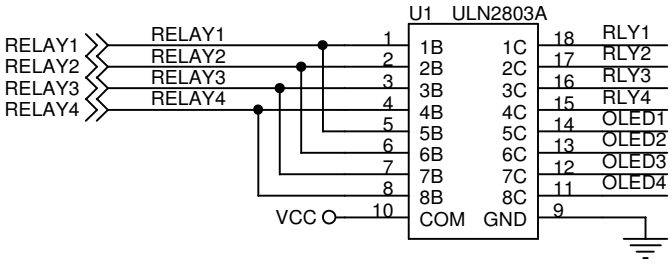
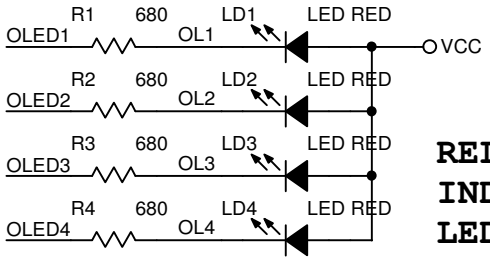

12 to 24 Volts DC
DC-DC Converter
INPUT TO +5V
D1
CD214A-B240LLF
DCIN
VIN+
DCC1 D-SUN
VIN2 VOUT2
VIN1 VOUT1
GND2 GND4
GND1 GND3
DC
D5
CD214A-B240LLF
2200uF 16V
VCC
C6
C7
1uF
TS8
TS 3.2
D2
P6KE43CA
C5
1000uF 35V
RST
SW1
SW PB
GP0
SW2
SW PB
VCC-WIFI
R15
10K
1%
1/8W
CHPD
VCC-WIFI
R16
10K
1%
1/8W
RST
R13 680
LD9
LED BLUE
PWRON
VCC
OPTIONAL +5V USB INPUT
CN1
CN-USB
D3
VUSB
VCC
CD214A-B240LLF
C8
1uF
AIN
Analog In
0 - 10V
R18
93.1K
1%
1/8W
ADC
D4
MMSZ4678
R21
10K
1%
1/8W
C18
1uF
ADC pin 0 - 1V
ESP8266 Radio Module
WF1
RST
ADC
CHPD
GP16 GP4
GP14 GP0
GP12 GP2
GP13 GP15
VCC GND
TXD
RXD
GP5
GP0
IN1
IN2
IN3
IN4
RELAY3
RELAY1
RELAY2
VCC-WIFI
RELAY4
ESP-07-12
R17 10K
RELAY1
R19 10K
RELAY2
R20 10K
RELAY3
R22 10K
RELAY4
RS-232 LEVEL CONVERTER
TXD
TSPARE
RXDO
R23
10K
1%
1/8W
T1_IN
T2_IN
R1_OUT
R2_OUT
TTL
T1_OUT
T2_OUT
R1_IN
R2_IN
RS-232
RS-TXD
RS-RXD
C13
1uF
C1+
C1-
C16
1uF
C2+
C2-
V+
V-
MV+
MV-
C14
1uF
C15
1uF
C17
1uF
VCC-WIFI
VCC
GND
U3
ST3232CDR
5 VOLT TO 3.3V REGULATOR
VCC
C9
10uF
C12
1uF
VIN
VOUT
ADJ
TAB
U4 TLV1117LV33DCYR
C10
10uF
C11
10uF
VCC-WIFI
J3
DB9F
RS-TXD
RS-RXD
RS-232 CONNECTOR
J4 HEADER 5X2
GP0
IN1
RXD
RST
TSPARE
TXD
RXDO
VCC
EXPANSION CONNECTOR
JUMPER 9-10 IF NOT USED
TO ENABLE RS-232 RECEIVER.
SPARE TERMINAL STRIP
S1
S2
TS9
TS 2.2
Digital Loggers, Inc.
2695 Walsh Av.
Santa Clara, CA 95051
Title
PLC 49
(c) 2015 by Digital Loggers, Inc.
Date Wednesday, September 30, 2015
Size A
Rev 1.2
Sheet 1 of 2
DWG NO
WiFi

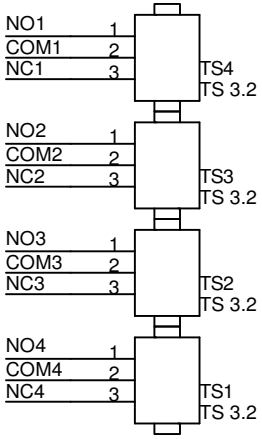
RELAY DRIVER

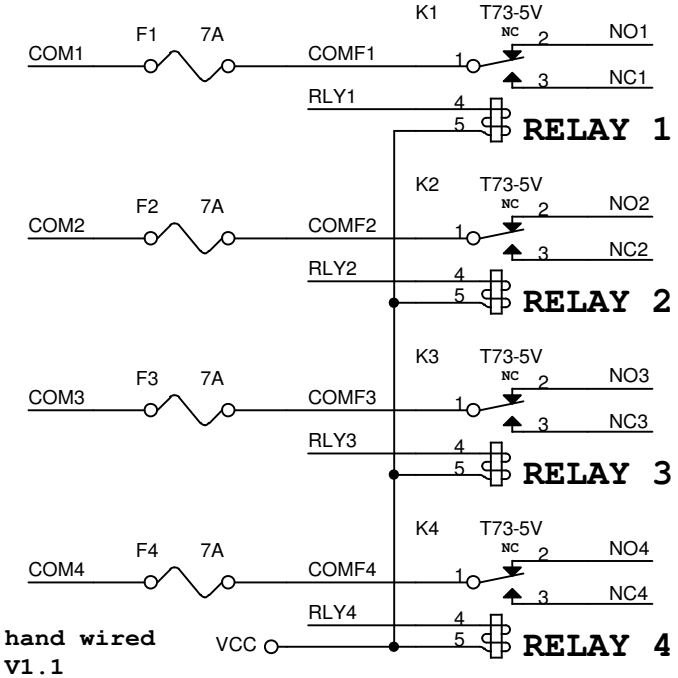




RELAY INDICATOR LED'S

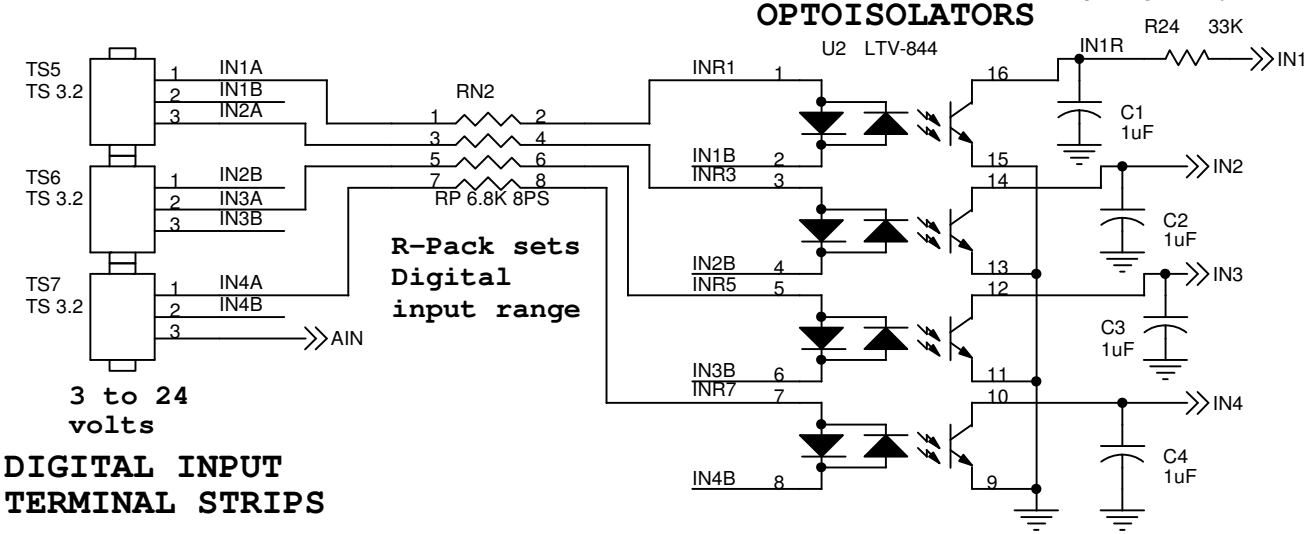
RELAY TERMINAL STRIPS





OPTOISOLATORS

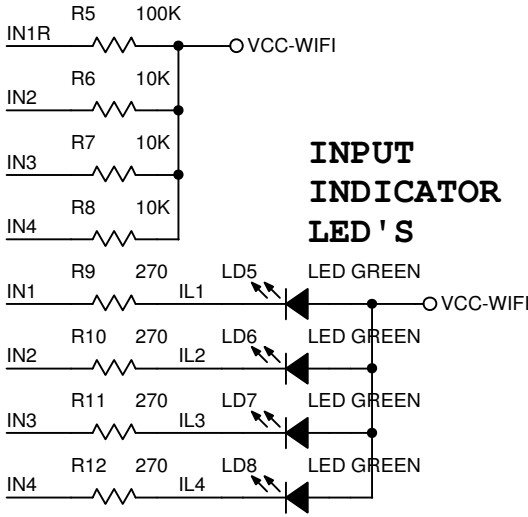
R24 is hand wired on PCB V1.1



R-Pack sets Digital input range

3 to 24 volts

DIGITAL INPUT TERMINAL STRIPS



INPUT INDICATOR LED'S

Note:

R24 is used to prevent entering a boot mode if input 1 is active at power up or reset.

Digital Loggers, Inc.

2695 Walsh Av.
Santa Clara, CA 95051

Title

PLC 49

Date	Size	Rev	Sheet	DWG NO
Wednesday, September 30, 2015	A	1.2	2 of 2	Relays